<u>S/N 09/470,329</u> <u>PATENT</u>

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brian R. Bennett et al. Examiner: Denise Tran

 Serial No.:
 09/470,329
 Group Art Unit: 2185

 Filed:
 December 22, 1999
 Docket No.: 884.174US1

Filed: December 22, 1999 Docket No.: 884.174US1
Title: PREVENTION OF LIVE-LOCK IN A MULTI-PROCESSOR SYSTEM

Assignee: Intel Corporation Customer Number: 21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.111

Mail Stop Amendment Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

In response to the Office Action mailed <u>December 18, 2006</u>, please amend the application as follows:

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a three-month extension of the period for responding to the Office action, thereby moving the deadline for response from March 18, 2007 to June 18, 2007.